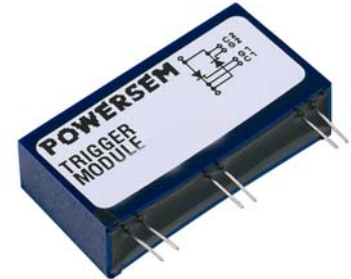
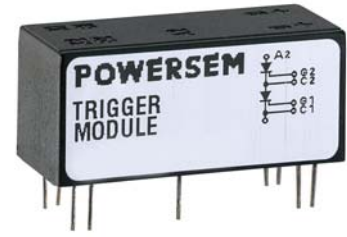


Active Trigger Units for Thyristors

- DIRECT CONNECTION TO THE OUTPUTS OF MICRO-CONTROLLERS, MICROPROCESSORS, CPLD'S AND FPGA'S IS POSSIBLE
- NO ADDITIONAL COMPONENTS NEEDED
- SMALL SIZE
- LONG GATE LEADS ARE POSSIBLE
- SIMILAR COSTS WITH CONVENTIONAL TRIGGER CIRCUITS DUE TO SAVING IN BOARD SPACE, SIZE IN POWER SUPPLY, SIZE IN SNUBBER NETWORK, OTHER COMPONENTS, LABOR AND INVENTORY
- PROVEN RELIABILITY IN VERY HARSH ENVIRONMENT OVER MANY YEARS



ELECTRICAL SPECIFICATIONS

Peak thyristor voltage	> 2000V (1200V or 2200V upon request)
Gate trigger current	> 1.2A with 2A/μs at 400V anode voltage typical
Control current	< 12 mA into optocoupler
Turn on delay	< 20 μs at 12 mA control current
Input-output insulation	> 800V peak continuous- 6KV 10s - according to VDE 0884
Temperature range	-25°C-+85°C

Types			Dimensions (mm) (top view)
V _{DRM}	2000Vpk	1200Vpk	
Phase control for two thyristors with two inputs (all applications) Weight = 24 g	PSTT PC/20	PSTT PC/12 upon request	
Single control input for two thyristors (AC Controller) Weight = 20 g	PSTT SC/20	PSTT SC/12 upon request	
Zero crossing for two thyristors Weight = 20 g	PSTT ZC/20	PSTT ZC/12 upon request	

PC= Phase Control
SC= Single Control
ZC= Zero Crossing
12= 1200Vpk (V_{DRM})
20= 2000Vpk (V_{DRM})

POWERSEM reserves the right to change limits, test conditions and dimensions – info@POWERSEM.net - www.POWERSEM.net

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